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**Christiansen**

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[54] **SYSTEM FOR DATA TRANSFER THROUGH AN I/O DEVICE USING A MEMORY ACCESS CONTROLLER WHICH RECEIVES AND STORES INDICATION OF A DATA STATUS SIGNAL**

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**Related U.S. Application Data**

[63] **Continuation-in-part of application No. 08/436,969, May 8, 1995, abandoned.**

[51] **Int. Cl.<sup>6</sup>** ..... **G06F 13/28**

[52] **U.S. Cl.** ..... **710/22; 710/17; 710/19**

[58] **Field of Search** ..... **348/385; 371/57.1; 395/200.62, 826, 842, 845, 115, 183.2; 607/30; 364/239.51; 370/445; 710/6, 22, 24; 714/5**

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[57] **ABSTRACT**

A method and system for transferring units of data between a computer memory and an external system in which a DMA controller stores and uses information from an I/O device interfacing with the external system to transfer data more efficiently.

**10 Claims, 7 Drawing Sheets**

